



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/655,596 Confirmation No.: 9320
Applicant : William F. Beausoleil, et al.
Filing Date : September 6, 2000
Title : High Speed Software Driven Emulator Comprised Of A Plurality of
Emulation Processors With A Method To Allow Memory Read/Writes
Without Interrupting The Emulation
Group Art Unit : 2124
Examiner : Tuan A. Vu
Docket No. : 706316-1203
Customer No. : 34313

Commissioner for Patents
Mail Stop Fee Amendment
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action mailed January 3, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.

CERTIFICATE OF MAILING
37 CFR §1.8

I hereby certify, pursuant to 37 CFR §1.8, that I have reasonable basis to expect that that this paper or fee (along with any referred to as being attached or enclosed) would be mailed or transmitted on or before the date indicated with the United States Postal Service with sufficient postage as first class mail on the date shown below in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Dated:

April 4, 2005

Todd Briggs

Todd Briggs